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## ABSTRACT OF THE DISCLOSURE

present invention discloses a control structure (i.e. a driving TFT) for reducing leakage in an display. Α semiconductor layer, such deposited on a polysilicon layer, is transparent substrate as a channel region. A lightly doped region and a drain region are disposed on one side of the polysilicon layer and a source region is disposed on the opposite side of the polysilicon layer. An insulating layer is. deposited covering the surface polysilicon layer, the lightly doped region, and the source/drain regions. Source and drain electrodes are disposed in the insulating layer, electrically connecting the source and drain region respectively. A gate metal disposed the insulating layer, layer is on approximately the top right portion of the polysilicon layer to form a transistor structure.